Ipul

MAR 3 1 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.:

10/700,391

Filed: N

November 4, 2003

Inventor(s):

Arun Radhakrishnan and Kelvin

D. Goveas

Title:

Prediction of Floating Point

Instruction Latency and Scheduling Based on

Prediction

Examiner:

Cody, Dillon J.

Group/Art Unit:

2183

Atty. Dkt. No:

5500-91300

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

Lawrence J. Merkel

7 Printed Name

1/19

Date

RESPONSE TO OFFICE ACTION OF March 6, 2006

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of March 6, 2006, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.